

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:

First Named Inventor: SHIMANUKI, Yoshihiko

Art Unit: 2812

Appln. No.: 10/519,785

Examiner:

Filed: September 6, 2005

Confirmation No.: 3053

For: SEMICONDUCTOR DEVICE AND ITS MANUFACTURING METHOD

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**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Supplementing the Information Disclosure Statement dated February 7, 2007, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO/SB/08A are hereby cited.

The listed documents were cited in a counterpart Japanese application.

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 (XA-10255) any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and has not been separately requested, such extension is hereby requested.

Respectfully submitted,

Date: June 9, 2008

By: /Mitchell W. Shapiro/  
Mitchell W. Shapiro  
Reg. No. 31,568

Miles & Stockbridge, P.C.  
1751 Pinnacle Drive  
Suite 500  
McLean, Virginia 22102-3833  
703-610-8652  
MWS:ecr